Total I	No.	of Questions :8] SEAT	No.:		
P182	2 0		[Total No. of Pages :2		
[5233] - 4002 M.Sc.					
COMPUTER SCIENCE					
CS - 403 : Embedded System					
(2013 Pattern) (Semester - IV)					
		(2013 i attern) (Semester - iv)			
Time .	:3	Hours]	I.	Max. Marl	ks :50
		ons to the candidates:			
1)		Answer any five questions.			
2) 3)		Neat diagram must be drawn whenever necessary. Figures to the right indicate full marks.			
	,				
Q1) a	a)	Draw a simple parallel I/O port and explain its basic	opera	tion.	[4]
ł	b)	Write short note on real time operating system.			[4]
C	c)	Define an embedded system.			[2]
`	<i>-</i>)	Donne un emocuaca system.			[-]
Q2) a	a)	Draw a neat diagram for the compilation process and	explai	in it briefl	y.[4]
1	1. \	E1-i- ITAC1 OCE 1ifl			f 41
ľ	b)	Explain JTAG and OnCE briefly.			[4]
(c)	What is a flash memory?			[2]
Q3) a	a)	Briefly explain the time slice mechanism for multitaski	ing ope	erating sys	
					[4]
ł	b)	Explain the basic buffer structure with suitable diagramment	ram.		[4]
(c)	List different software tools required for designing a	n emb	edded sys	stem.

[2]

How is a real time performance derived from a non-real time system?[4] **Q4**) a) [4] b) Write a note on cross compilers. c) Define maskable and non-maskable interrupts. [2] **Q5**) a) Explain the high level language simulation. [4] b) Explain the read and write cycles of an DRAM cell with suitable diagram. **[4]** What is a peripheral? List any two types of peripherals. [2] c) Briefly explain the preemptive and non preemptive kernel. **Q6**) a) [4] Write short note on Circular buffer. b) [4] Define Page and Segment. c) [2] What are the advantages and disadvantages of buffer exchange? **Q7**) a) [5] Explain priority levels for Real Time Operating system architecture. [5] b) Explain the 'Serial Lines' and 'From disk' methods of downloading the **Q8)** a) code to the target board. [5] Explain task level and symbolic debugger. b) [5]

